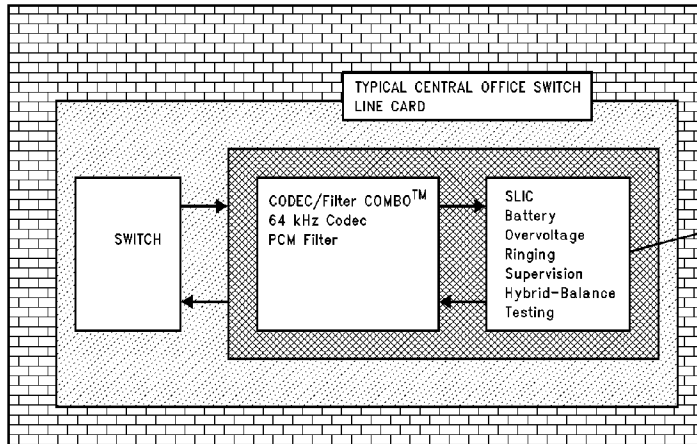


Subscriber Line Card for Central Office Telephone Switching Equipment

National Semiconductor
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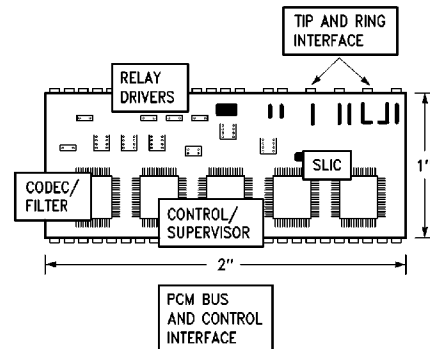
SYSTEM DESCRIPTION

Subscriber linecards are designed to interface the analog subscriber line to a PCM highway in a Digital Central Office or a Digital Loop Carrier (DLC) channel bank or remote concentrator systems. Subscriber Line Interface Module (SLIM™) devices are complete Electronic Subscriber Line Interface Circuit (SLIC) and PCM COMBO® CODEC/Filter systems, designed to meet the requirements for POTS (Plain Old Telephone Service) lines for Central Office or DLC subscriber line cards. When used in conjunction with a simple, non-critical, external protection network, two feed resistors, and a ring relay, the SLIM forms a complete line circuit handling all of the BORSCHT functions.

The SLIM consists of a line driver, a line receiver, a line impedance control circuit, a hybrid balance circuit, a loop supervision circuit, a ring supervision circuit, three positive relay drivers, a TP3054 COMBO CODEC/Filter, and a serial control interface.

The SLIM is assembled in a 1" by 2" Dual-In-Line Package, which allows very high density line cards with many lines per card and typically can reduce the card to card spacing within a switch rack or frame.

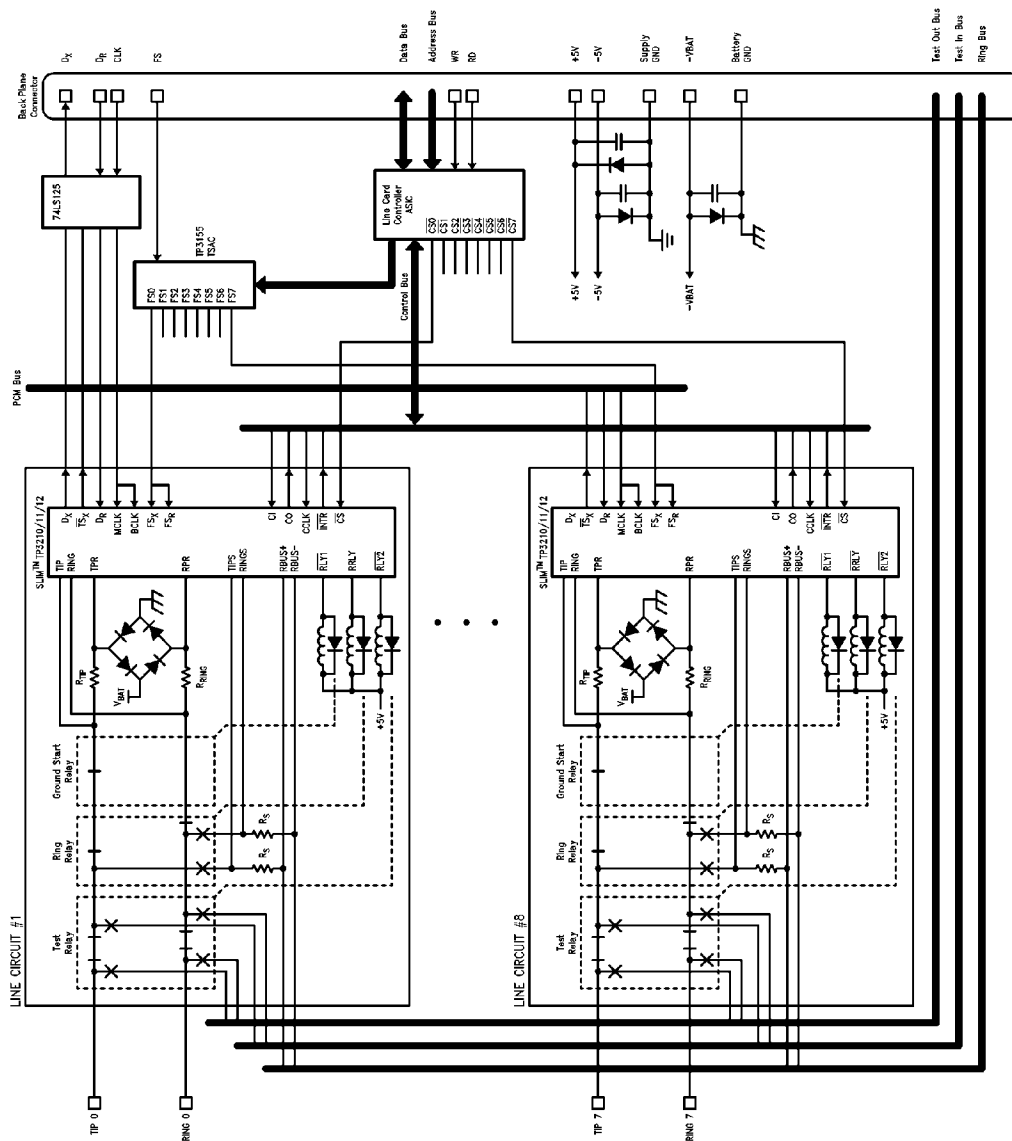
SLIM-Subscriber Line Interface Module



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COMBO® is a registered trademark of National Semiconductor Corporation.
SLIM™ is a trademark of National Semiconductor Corporation.

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KEY DESIGN CHALLENGES**Meeting All Bellcore/REA/CCITT Specifications**

Designing a Subscriber Line circuit which meets all of the Bellcore/REA/CCITT specifications not only requires tremendous awareness of the specifications, but when understood, can be extremely difficult to meet from a hardware point of view. Discrete solutions typically used require the precise evaluation of each component and careful system planning and design to insure compliance to all of the Bellcore/REA/CCITT specifications. The SLIM, being a complete line circuit system in a single package which is designed by National Semiconductor to meet the specifications, minimizes the demands on the designer and the technical risk associated with discrete multi-component approaches.

Meeting System Cost Requirements

The Switching System Equipment market is highly competitive and therefore the demands on the designer to use the least cost approach is ever dominant. The SLIM is competitive with all other modern alternatives, and due to its highly integrated system level approach, it minimizes the component incoming test, linecard assembly and yield loss factors, thereby reducing total cost of ownership for the linecard solution.

Time to Market

Typical turn around time from design start through field trials and production release can be very lengthy and therefore is dangerous in terms of keeping a competitive edge or stay-

ing competitive. The SLIM, being a complete line circuit "system" in a single package, reduces the engineering design time to system and line card evaluation time, thus significantly decreasing the time to market.

KEY FEATURES OF THE TP3210, TP3211, TP3212 SLIM SUBSCRIBER LINE INTERFACE MODULE

- Complete CODEC/Filter and SLIC functions plus protection
- Requires only simple protection network and 4 resistors externally
- Very small 1" by 2" package supports high density line card and system
- Superb power surge and lightning protection
- Withstands 500V Return to Ground surge
- Power Denial mode
- Thermal overload protection
- Automatic Ring Trip
- Four Selectable Balance Networks
- Three positive relay drivers
- TP3210 SLIM meets all Bellcore and REA specifications for USA Central Office
- TP3211 SLIM meets all CCITT requirements for 600 Ω Central Office applications
- TP3212 meets TR-TSY-00057 specification for DLC POTS lines

Typical Bill of Material for an 8-Channel SLIM Based Line Card

Function	Description	NSC Part	Other Mfg	Qty
SLIC + CODEC/Filter Line Circuit Protection	SLIM	TP3210/11/12		8
	Diode Bridge			8
	Fusistors	2 x 100 Ω , $\pm 3\%$		8
Ring Feed Relays	Resistors	2 x 360 Ω , $\pm 3\%$		8
	Ring Relay		DS-2E-5VDC	8
	Test Relay		DS-4E-5VDC	8
	Ground Start Relay (Note 1)		DS-2E-5VDC	8
Relay Catch Diodes			1N4001	24
Time Slot Assigner	TSAC	TP3155		1
Line Card Controller		ASIC (Note 2)		1
Buffer		74LS125		1
Supplies Filter and Protection	Schottky Diodes		1N5820	2
			1N6290A	1
	Tantalum Capacitors		47 μ F, 25V	2
	Electrolytic Capacitor		47 μ F, 63V	1

Note 1: Ground Start Relay is Optional depending on application.

Note 2: Line Card Controller ASIC is dependent on backplane structure.

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